

CLAIMS

1. An apparatus for dualizing an Asynchronous Transfer Mode (ATM) router in a CDMA2000 system, the apparatus comprising:
  - 5 a first ADSL Subscriber Physical board Assembly (ASPA) board; and a second ASPA board; wherein the first ASPA board comprises a first disk-on-chip for storing configuration and operation information; a first memory for storing an executable format of an object associated with the configuration and operation information; a first dualized processor for transmitting the configuration and operation information to the second ASPA board or receiving the configuration and operation information from the second ASPA board using File Transfer Protocol (FTP); and a first operation and maintenance processor for storing the configuration and operation information in the first disk-on-chip and generating an object executable on the memory from the information stored in the first disk-on-chip,
  - 10 and wherein the second ASPA board comprises a second disk-on-chip for storing configuration and operation information; a second memory for storing an executable format of an object associated with the configuration and operation information; a second dualized processor for transmitting the configuration and operation information to the first ASPA board or receiving the configuration and operation information from the first ASPA board using the FTP; and a second operation and maintenance processor for storing the configuration and operation information in the second disk-on-chip and generating an object executable on the memory from the information stored in the second disk-on-chip.
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2. A method for dualizing an ATM router in a CDMA2000 system, the method comprising the steps of:
  - 25 at a dualized processor, determining in which of three states an ASPA board is, wherein the three states are Single, Active and Standby;
  - 30 if it is determined that the ASPA board is in the Single or Active state,
    - at the dualized processor, initiating an operation and maintenance processor;
    - 35 at the operation and maintenance processor, receiving messages from managed cards, generating an configuration and operation information therefrom and storing the configuration and operation information in a disk-on-chip; and

at the operation and maintenance processor, generating an object executable on a memory from the configuration and operation information and notifying the other ASPA board of the object generation when generating the object is completed,

5 or if it is determined that the ASPA board is in the Standby state,

at the dualized processor, waiting until receiving the notification of the generation completed;

10 at the dualized processor, upon receiving the notification, receiving the configuration and operation information stored in the disk-on-chip of the other ASPA board using a FTP, and storing the information in a disk-on-chip of the ASPA board where the dualized processor is mounted;

at the dualized processor, initiating an operation and maintenance processor; and

15 at the operation and maintenance processor, generating an object executable on a memory from the configuration and operation information.